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Debbie Peloquin

Name of Person Mailing Correspondence

addressed to the Assistant Commissioner for Patents, Washington, D. C. 20231



### AMENDMENT

# In the Claims:

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Claims:

Presented below are the amended claims in a clean, unmarked versions.

Characteristics all pending claims are presented herein. For the Examiner's convenience all pending claims are presented herein.

Please cancel claims 2-1/9, and 2/1-32.

# Please amend the claims as follows:

(Twice Amended) A method comprising:

placing an incomplete chip package into a mold formed by a first portion and a second portion, the incomplete chip package comprising a chip and a substrate electrically coupled using a flip chip process, the mold having an upper inner surface in which its entire length is coated with release film, and the chip having (i) a top surface facing the substrate, (ii) a bottom surface opposite the top surface, the bottom surface butting against the upper inner surface, and (iii) one or more side surfaces between the top and bottom surfaces;

injecting a liquid resin into a runner section of the mold, the runner formed between a first portion and the second portion, and the resin encapsulating a significant portion of the one or more side surfaces, and filling a first gap between the top surface and the adjacent substrate; and

Docket No.: 42390P9482 Application No.: 09/741,535 curing the resin.

control

20. (Twice Amended) A method comprising:

placing an incomplete flip chip package into a bottom inner cavity of a bottom mold portion, the incomplete flip chip package comprising a chip and a substrate, the chip having a top surface coupled by reflowed solder bumps to an upper surface of the substrate, the chip further comprising a bottom surface opposite the top surface and one or more side surfaces between the top and bottom surfaces;

mating an upper mold portion with the lower mold portion, the upper mold portion having an upper inner cavity, including an upper inner surface in which its entire length is coated with a release film, and the bottom surface of the chip butts against the upper inner surface, the upper and bottom inner cavities forming a mold inner cavity enclosing the incomplete flip chip package, and forming a runner between the upper and lower mold portions,;

injecting a predetermined amount of a liquid resin into the mold inner cavity through the runner, the liquid resin encapsulating substantially all or the one or more side surfaces and substantially all of the upper surface, the liquid resin further filling a gap between the top surface of the chip and an adjacent portion of the upper

Docket No.: 42390P9482 Application No.: 09/741,535 surface of the substrate, encapsulating the reflowed solder bumps; and

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curing the liquid resin by maintaining the mold at an elevated temperature for a predetermined period of time, the elevated temperature being equal to or greater than the cure temperature of the filled liquid resin for the predetermined period of time.

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# Please add the following new claims:

33. (New) The method of claim 1, additionally comprising separating the mold between the upper mold portion and the lower mold portion to expose a molded chip package, the upper mold portion being removed with the release film.

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- (New) The method of claim 20, additionally comprising separating the mold between the upper mold portion and the lower mold portion to expose a molded chip package, the upper mold portion being removed with the release film.
- 35. (New) A molded flip chip package comprising solidified resin formed by:

  placing an incomplete chip package into a mold formed by a first portion

  and a second portion, the incomplete chip package comprising a

  chip and a substrate electrically coupled using a flip chip process,

  the mold having an upper inner surface in which its entire length is

Docket No: 42390P9482 Application No: 09/741,535 coated with release film, and the chip having (i) a top surface facing
the substrate, (ii) a bottom surface opposite the top surface, the
bottom surface butting against the upper inner surface, and (iii) one
or more side surfaces between the top and bottom surfaces;

injecting a liquid resin into a runner section of the mold, the runner formed
between a first portion and the second portion, and the resin
encapsulating a significant portion of the one or more side surfaces,
and filling a first gap between the top surface and the adjacent
substrate; and

curing the resin.

1 36. (New) The method of claim 35, additionally comprising separating the
2 mold between the upper mold portion and the lower mold portion to
3 expose a molded chip package, the upper mold portion being removed

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with the release film.

### REMARK

Applicants respectfully request reconsideration of this application as amended. Claims 1 and 20 have been amended; claims 2-19, 21-26, and 31-32 have been cancelled; and claims 33-36 have been added. Therefore, claims 1, 20, and 33-36 are now presented for examination.

The Applicants would like to thank the Examiner for the interview conducted on August 21, 2002. While the Examiner determined during the interview that the amended and new claims are not anticipated by the prior art of record, and will require a new search, the arguments presented herein are directed to the prior art of record for the purpose of removing them as prior art references.

Claims 1, 20, and 33-36 are not anticipated by nor made obvious by Carney (U.S. Patent No. 5,895,229) or Lin (U.S. Patent No. 5,450,283), individually, or in combination

As pointed out during the Examiner interview, neither Carney nor Lin teaches or discloses at the least the following elements that are required by the Applicants' invention as recited by the claims:

- release film that is coated along the entire length of the upper inner surface of the upper inner cavity
- an upper inner surface that is coated with release film
- release film that is removed when the mold is separated

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In Lin, a tape is placed in the mold cavity such that the tape makes contact with the backside of the die, but not with the entire length of the top mold platen (see reference numeral 38 in FIGS. 3 and 4). In Lin, tape is not needed along the entire length of the top mold platen because the structure of the mold is such that the tape is not needed along the entire length to aid in the release of the mold. (See Lin, column 4, lines 51-53.) In contrast, in the Applicants' invention as recited by the claims, the release film is coated along the entire length of the upper inner surface of the top portion of the mold, aiding in the separation of the mold.

Secondly, Lin does not teach or disclose that the upper mold surface is lined with tape. It discloses that a "layer of tape 38 is <u>placed inside the mold cavity</u> so that the tape 38 makes contact with the inactive backside" (emphasis added). In contrast, the Applicants' invention as recited by the claims, and as taught by the original specification, requires that the release film be placed on the upper mold surface, not just in the upper mold cavity as in Lin.

Thirdly, Lin does not teach or disclose that when the mold is separated, that the tape is removed along with the mold. Instead, in Lin, the tape must be separately removed (see FIG. 5), since its properties are such that it "adheres better to silicon than to the top mold platen" (Lin, column 4, lines 28-29). In contrast, in the Applicants' invention as recited by the claims, and as taught by the original specification, requires that when the mold is separated, that the release film is removed with it such that the process eliminates the extra step of having to remove the release film from the molded chip package.

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## Conclusion

Applicants respectfully submit that the claim amendments, and the new claims do not add any new subject matter, and are all supported by the original specification. Furthermore, the Applicants submit that any previous rejections have been overcome by the amendment and remark, and that the claims are now in condition for allowance if no further art is found. Accordingly, Applicants respectfully request that the presented claims be allowed.

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# Invitation for a Telephone Interview

The Examiner is requested to call the undersigned at (303) 740-1980 if there remains any issue with allowance of the case.

# Request for an Extension of Time

The Applicants respectfully petition for an extension of time to respond to the outstanding Office Action pursuant to 37 C.F.R. § 1.136(a) should one be necessary. Please charge our Deposit Account No. 02-2666 to cover the necessary fee under 37 C.F.R. § 1.17(a) for such an extension.

# **Charge our Deposit Account**

Please charge any shortage to our Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

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